

FIGURE 3

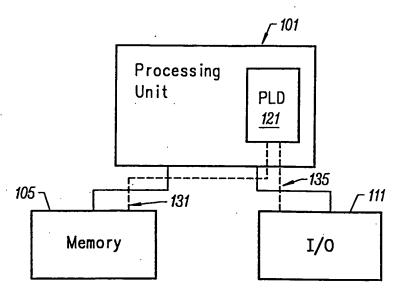


FIGURE 1

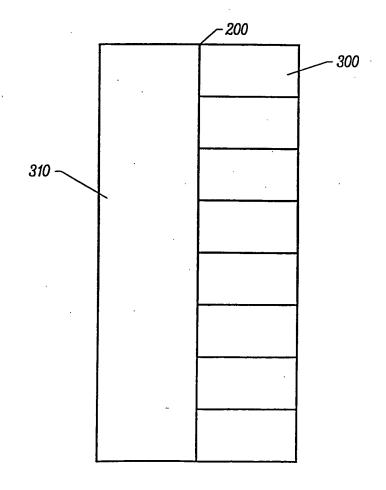


FIGURE 4

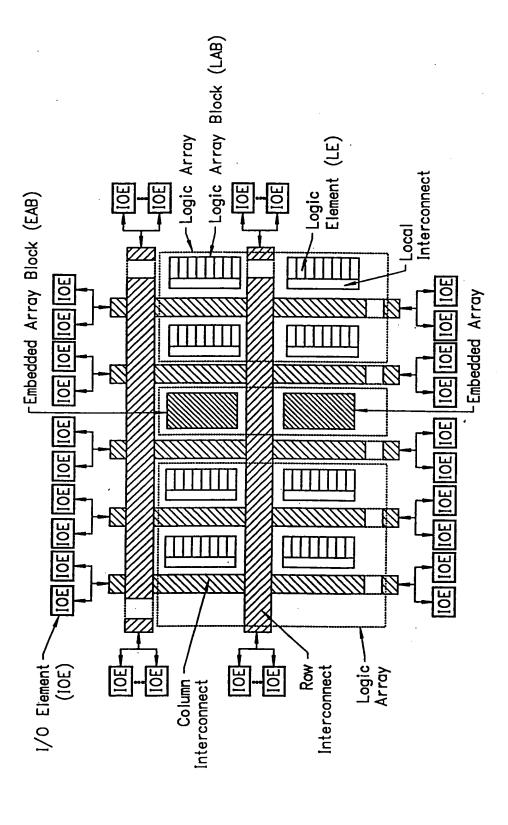


FIGURE 5

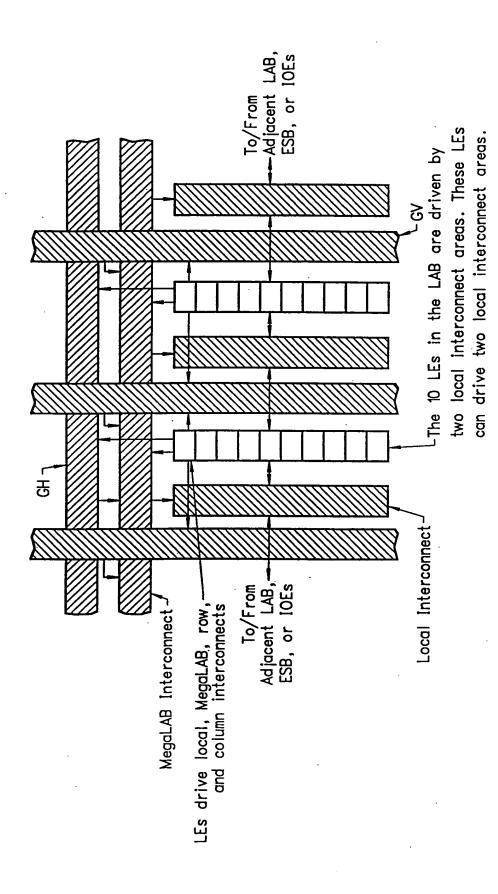


FIGURE 6

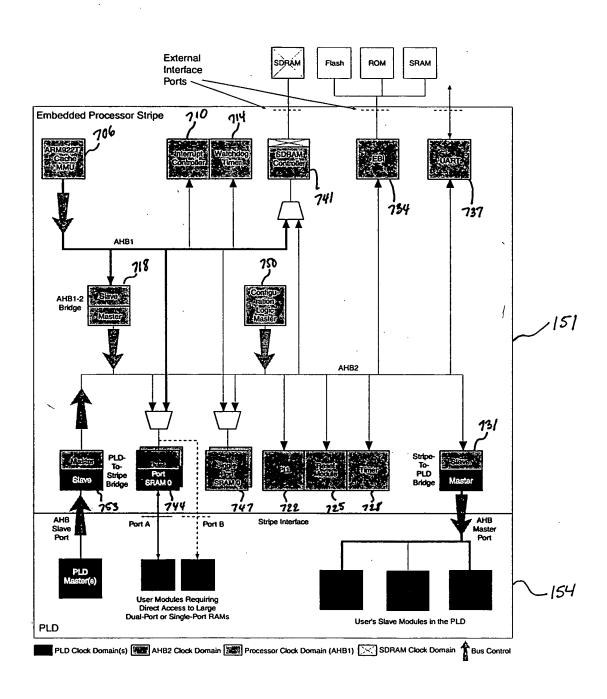


FIGURE 7

PROGRAMMABLE LOGIC

FIGURE 9

EMBEODED PROCESSOR STRIPE

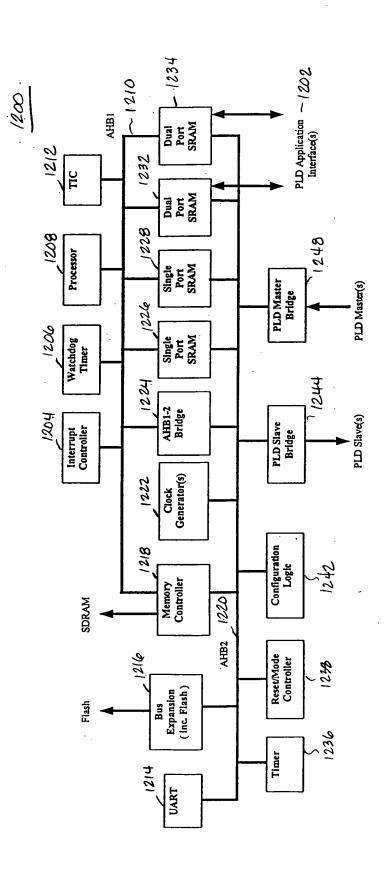
FIGURE 10

PROGRAMMABLE LOGIC

FIGURE 11

EMBEODED
PROCESSOE STRIFE
PROGRAMMABLE
LOGIC

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FlysRE 12

32 8 K 8K.x32 16 4K 16K 16K×16 8 нK 8 4 32 K 32 KX8

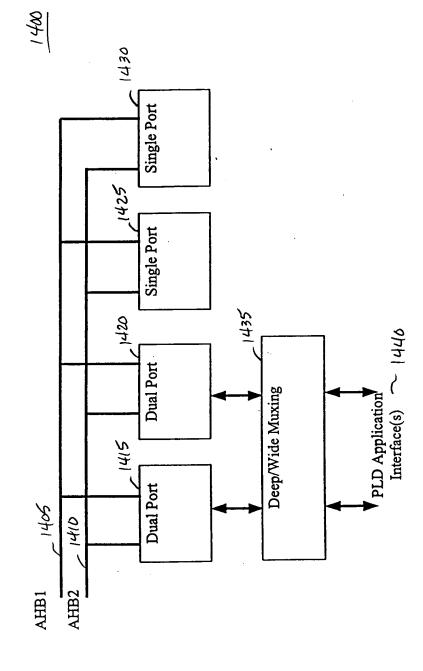
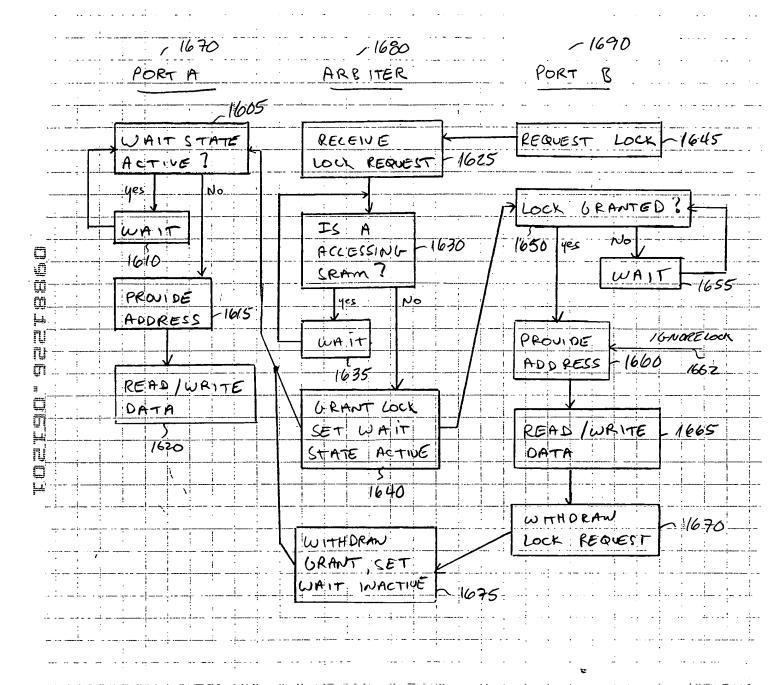


FiguRE 14

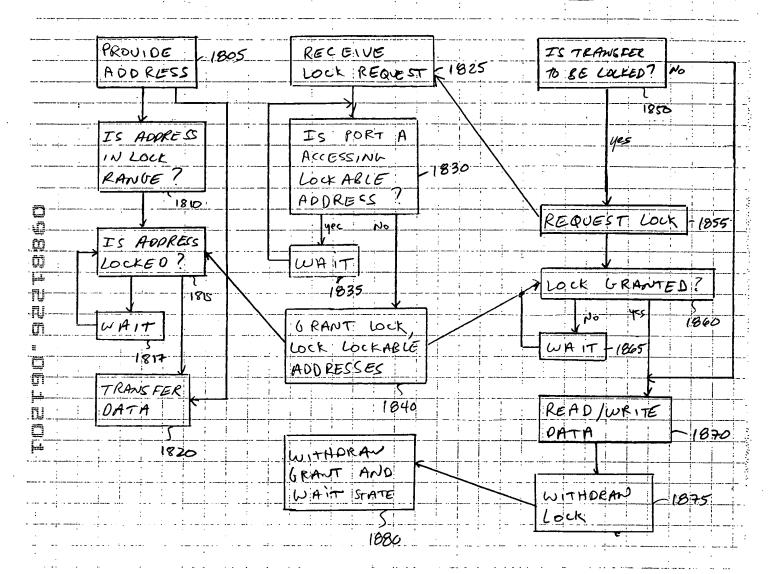
ACCESS IN 1505				LOCK REOUL	37
PROURESS			-1525		
	<u> </u>	ARBITER		Lock GRAN	ケ ・
WAIT	1515		1535	: 	
PORT A			20	PORT B	
			:	<u> </u>	
ADDRESS				ADDRESS	
	1540	DVAL	155	5	
WRITE		PORT		WRITE	
DATA	1555	SKAM	-1560	DATA	·
READ			1565	READ	
DATA	1560			DATA	
4			·		_
1570				80	
	· · · · · · · · · · · · · · · · · · ·			· · · · · · · · · · · · · · · · · · ·	, <u></u>
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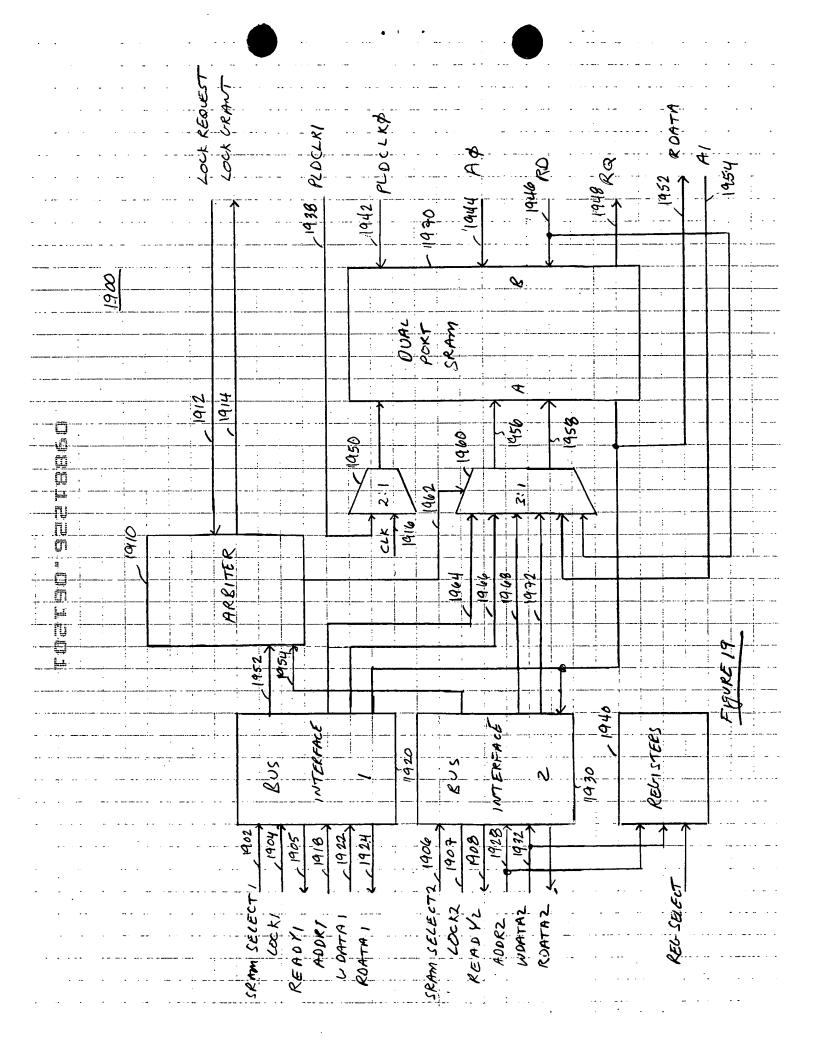
1705	1710	
PROUPESS	LOCK SIZE 1912	LOCK FRANT
WA IT 1715	ARGIJER	(735
	1785	
PORT A		PORT B
ADDRESS 1740	LOUKABLE LOUKABLE	ADDRES
U U URITE 可 DATA 1745	1780	WRITE
T READ	IN DE PENDENT	-1760 READ
□ DATA 1750 □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □		1765 DATA
7	DUAL PORT SRAM	1780
	FYURE 17	
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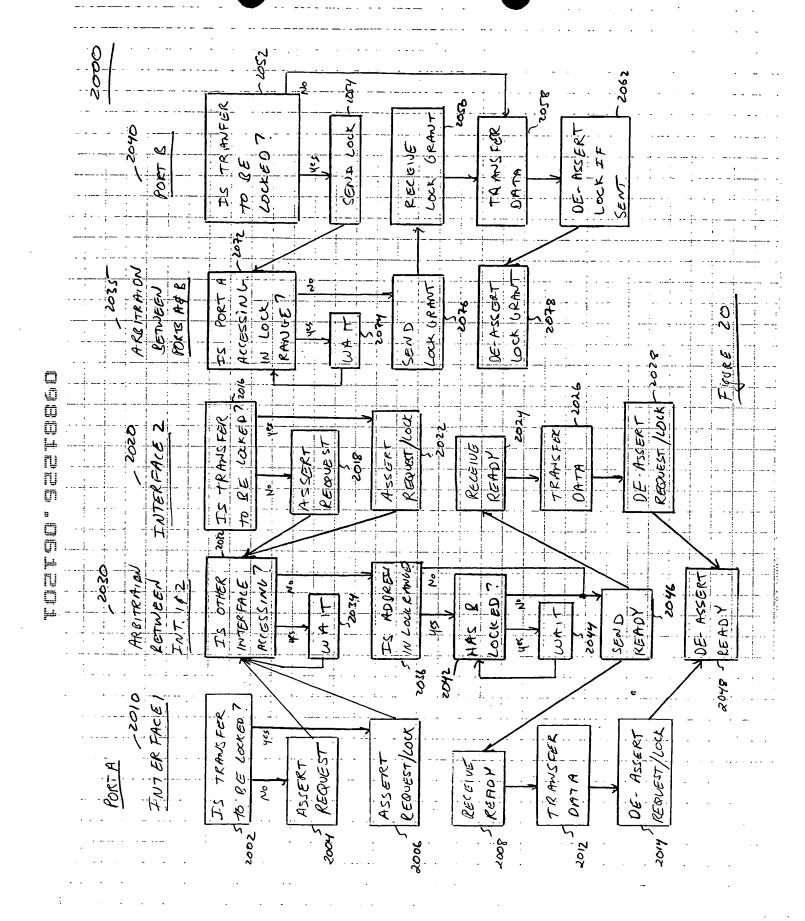
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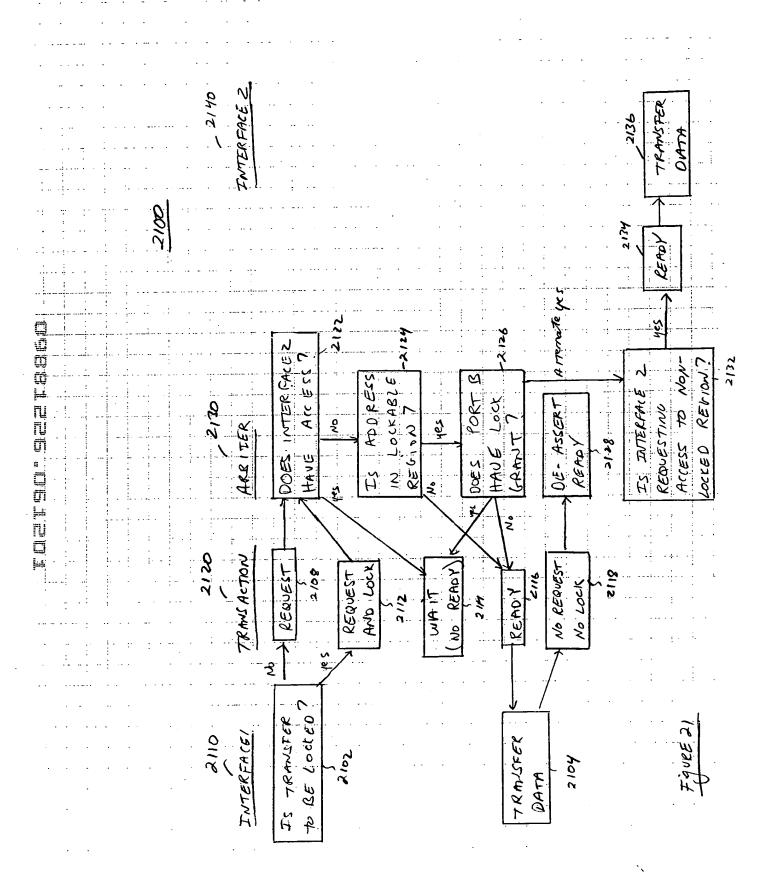
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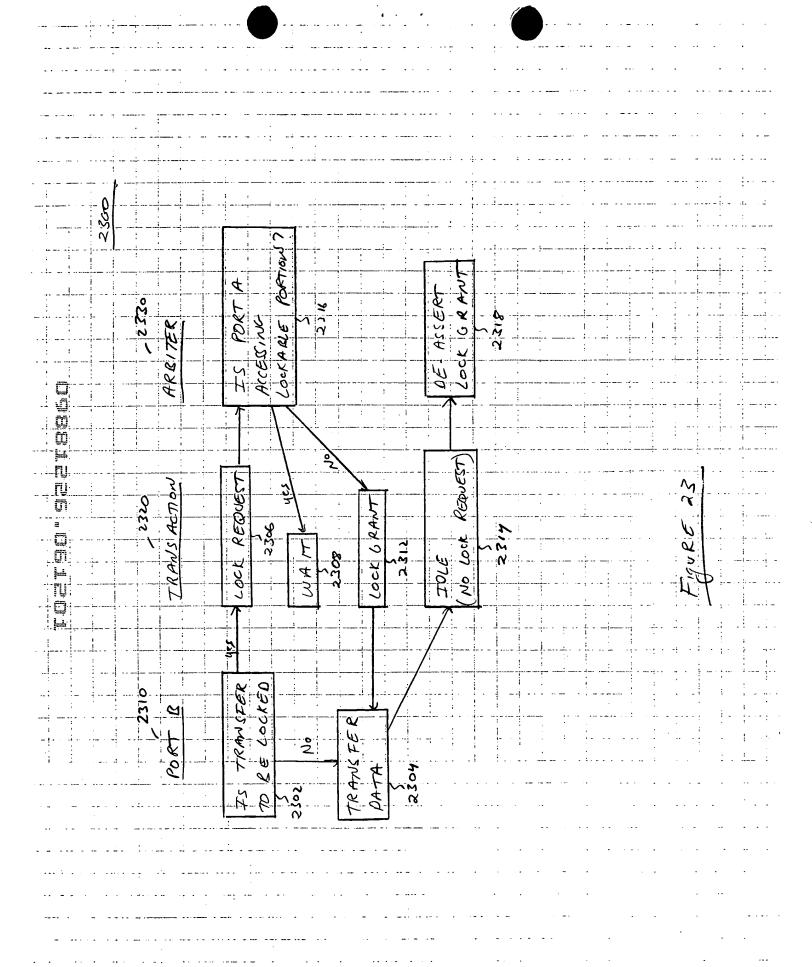


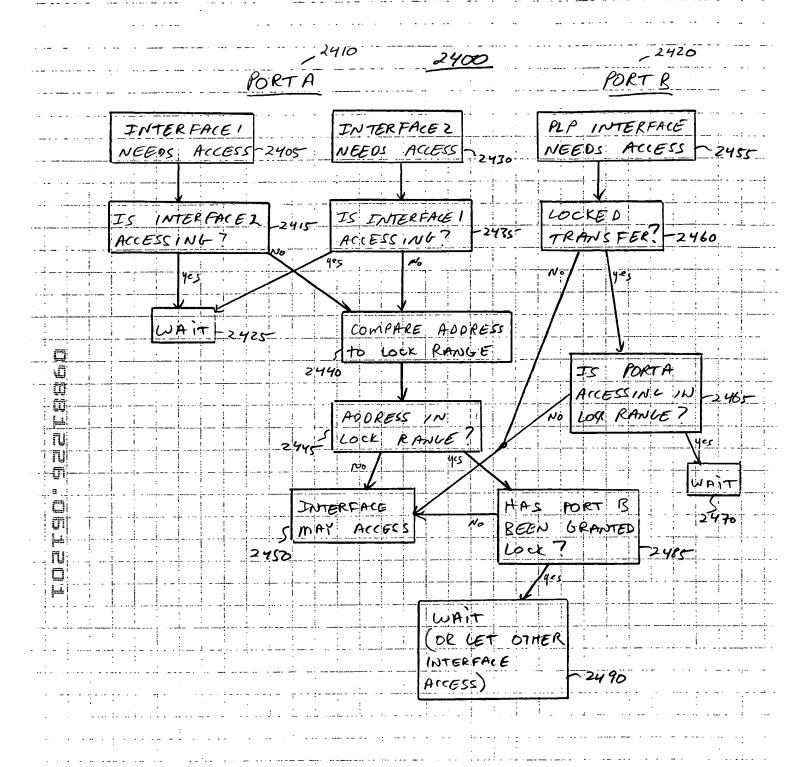
FOURE 18











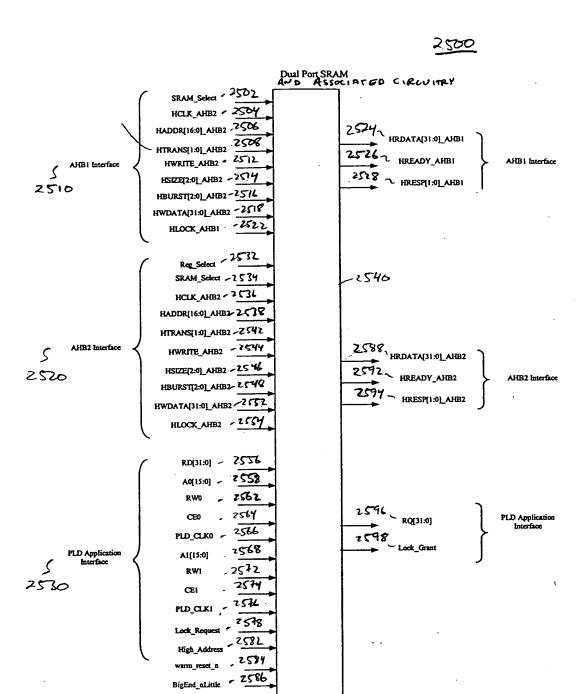


FIGURE 25